

Three important recommendation before testing Logic discrimination in SEPAM 40,60,80:

Please considered following condition to have a successful Logic discrimination performance:

- Circuit Breaker status should be closed, please see the FAQ written about this important subject- refer to FAQ No. **FA253371**,
- All auxiliary power supply should have common negative point,
- The minimum time delay recommendation for logic discrimination should be respected. We do recommend to set the its time delay minimum **100ms**. Otherwise the correct performance of logic discrimination is not guaranteed.

AEDL3