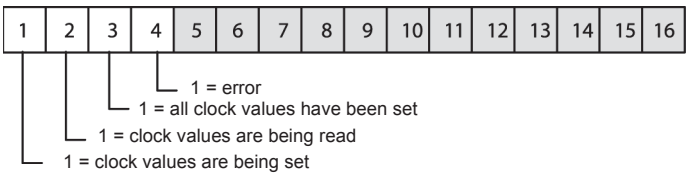
Time of Day Clock

When a 4x holding register assignment is made in the configurator for the time of day (TOD) clock, that register and the next seven consecutive registers (4x ... 4x + 7) are set aside in the configuration to store TOD information. The block of registers is implemented as follows.

Register	Definition
4X	The control register: 
4X+1	Day of the week (Sunday = 1, Monday = 2, etc.)
4x+2	Month of the year (Jan. = 1, Feb. = 2, etc.)
4x+3	Day of the month (1... 31)
4x+4	Year (00... 99)
4x+5	Hour in military time (0... 23)
4x+6	Minute (0... 59)
4x+7	Second (0... 59) When a 4x holding register assignment is made in the configurator for the time of day (TOD) clock, that register and the next seven consecutive registers (4x... 4x + 7) are set aside in the configuration to store TOD information.

The block of registers is implemented as follows. For example, if you configured register 40500 for your TOD clock, set the bits appropriately as shown above, then read the clock values at 9:25:30 on Tuesday, July 16, 1991, the register values displayed in decimal format would read:

Register	Definition
400500	0110000000000000
400501	3 (decimal)
400502	7 (decimal)
400503	16 (decimal)
400504	91 (decimal)
400505	9 (decimal)
400506	25 (decimal
400507	30 (decimal)