

Smart-UPS SRT Statement of Volatility (SoV)

The Smart-UPS product line does not contain any user addressable non-volatile memory. The UPS does contain EEPROM but this memory is only addressed by the internal microprocessor and it's not accessible to the user for any random memory storage operation. There are 2 sets of data stored in this memory, user preferences and settings for operation, as well as operational parameters needed for the device to operate properly.

The memory inside Smart-UPS units can be broken down as follows:

- On-Chip Data RAM for internal MPU registers. Not accessible to peripherals.
- On-Chip Flash for UPS Firmware. This firmware can be updated using a special process with valid images generated by APC. There are security measures to prevent unauthorized images from being loaded.
- On-board EEPROM used by the MPU to store operating parameters. Data can only be stored into the EEPROM by the MPU. The EEPROM is not directly accessible to the user. When in factory-program mode, it can accept a limited number of user-selectable choices related to the unit's functionality, and that subset can only be programmed through APC Power Chute software, APC Network Enhancement devices, or the local user interface (if the unit has one).

In case you need them, the above acronyms stand for the following:

- RAM: Random Access Memory.
- MPU: Microprocessor Unit.
- EPROM: Erasable Programmable Read-Only Memory.
- EEPROM (or E2PROM): Electrically Erasable Programmable Read-Only Memory

Smart-UPS SRT models except Smart-UPS Ultra models 1-10kVA:

Memory	Number	Sizes	Location
EEPROM	2	64KB and 32MB	on board
Flash	2	512KB and 2MB	in microprocessor
RAM	2	256KB and 512KB	in microprocessor